

DIGITAL FILTER

Patent Number: JP4222111
Publication date: 1992-08-12
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Requested Patent: ☐ JP4222111
Application Number: JP19900413390 19901221
Priority Number(s):
IPC Classification: H03H17/02 ; H03H17/06
EC Classification:
Equivalents:

14-22211

Abstract

PURPOSE: To obtain the compact and high-speed digital filter to easily change a filter coefficient.
CONSTITUTION: A multiplier 12 multiplies a coefficient signal 2 and the output of a counter 12, which values are increased within a range available for an input signal 1. to be multiplied with this coefficient signal 2, and the multiplied results are successively stored in the addresses, which are decided by the count values as mentioned above, of a storing means 5 selected by a storing means selecting means 6. The input signal 1 is decoded by an address decoder 8, and the product of the above-mentioned input signal and the above-mentioned coefficient signal is read out from the above-mentioned storing means 5.

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